

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert S. Weiner

Title: Non-volatile transistor memory array incorporating read-only elements with single mask set

Docket No.: 2800.450US1
Filed: March 26, 2004
Examiner: Thinh Nguyen
Customer No.: 76287

Serial No.: 10/810,035
Due Date: July 9, 2010
Group Art Unit: 2818
Confirmation No.: 8458

Notice of Allowance Date: April 9, 2010

Mail Stop Issue Fee

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following:

- ☒ Issue Fee Transmittal (Form PTOL-85).
- ☒ Request to Apply Previously Paid Issue Fee (1 pg.)
- ☒ Communication Re: Fee Address (1 page).

Please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.

Customer No.: 76287

REM:CMG:fws

By



Robert E. Mates

Reg. No. 35,271

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 9 day of July, 2010.

Frederik Struve

Name


Signature